

\* Abstract provided for the Examiner's convenience

## INFORMATION DISCLOSURE CITATION

## IN THIS APPLICATION

(Use several sheets if necessary)

AUG 26 2004

Attorney Docket No.:

8008-1050

Application No.:

10/786,552

Applicant:

Hiroyushi KUGE et al.

Filing Date:

February 26, 2004

Group Art Unit:

2825

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing date (if appropriate)
W	2002/0004929	1/10/2002	Osaki et al.			
	6,145,116	11/7/2000	Tawada			
	5,509,019	4/16/1996	Yamamura			

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
W	2000022081 (English abstract only)	1/21/2000	JAPAN - ABSTRACT				
	10144796 (with English abstract)	5/29/1998	JAPAN - ABSTRACT				X

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

W	Jones, T.R.: "JTAG CLOCK & CONTROL SIGNAL DISTRIBUTION SCHEME" Motorola Technical Developments, Motorola Inc. Schaumburg, Illinois, US, vol. 18, 1 March 1993, pages 44-49.

EXAMINER:

WJN

DATE CONSIDERED

7/14/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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